



(13)

Match and Return

PATENT

BC
#4/B
KWS
1-703

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ochoa et al.

Serial No.: 09/940,010

Filed: August 27, 2001

For: SEMICONDUCTOR INTEGRATED
CIRCUIT HAVING COMPRESSION
CIRCUITRY FOR COMPRESSING TEST
DATA, AND THE TEST SYSTEM AND
METHOD FOR UTILIZING THE
SEMICONDUCTOR INTEGRATED
CIRCUIT

Confirmation No.: 3702

Examiner: S. Chase

Group Art Unit: 2133

Attorney Docket No.: 3011.3US
(92-0657.4)

Notice of Allowance Mailed:

September 10, 2002

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EY209816220US

Date of Deposit with USPS: December 3, 2002

Person making Deposit: Matthew Wootton

AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Box ISSUE FEE
Commissioner for Patents
Washington, D.C. 20231

Sir:

Please amend the above-referenced application as follows:

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